

BUR9-1999-0300US1

09/691,353

01240162aa

1

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In repatent application of

James W. Adkisson, et al.

Serial No. 09/691,353

Group Art Unit 2823

Filed October 18, 2000

Examiner K. Nguyen

For METHOD OF FABRICATING SEMICONDUCTOR SIDE WALL FIN

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

DECLARATION UNDER 37 C.F.R. §1.132

OF

JAMES W. ADKISSON, PAUL D. AGNELLO, ARNE W. BALLANTINE and JED H. RANKIN

Sir:

JAMES W. ADKISSON, PAUL D. AGNELLO, ARNE W. BALLANTINE and JED H. RANKIN declare as follows:

- 1. We are co-inventors of U.S. Patent No. 6,563,131 to Adkisson et al. ("Adkisson '131"), and are the only inventors of Adkisson '131 who are also inventors of the present invention.
- 2. With respect to Adkisson '131, any subject matter disclosed but not claimed in Adkisson '131 and described in the present invention was derived solely from James W. Adkisson, Paul D. Agnello, Arne W. Ballantine and Jed H. Rankin, who are co-inventors of the present invention.
  - 3. We further declare that all statements made herein of our own

BUR9-1999-0300US1

09/691,353

01240162aa

2

knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the above referenced application and any patent issuing thereon.

Date: December 6, 2005	The Comments
. <i>v</i>	JAMES W. ADKISSON
Date;	
	PAUL D. AGNELLO
Date:	
	ARNE W. BALLANTINE
Date:	
	JED H. RANKIN



BUR9-1999-0300US1

09/691,353

01240162aa

2

knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the above referenced application and any patent issuing thereon.

Date:	
	JAMES W. ADKISSON
Date:	
	PAUL D. AGNELLO
Date:	
	ARNE W. BALLONTINE
Date: 12/7/05	Del Ille
	JED H. RANKIN